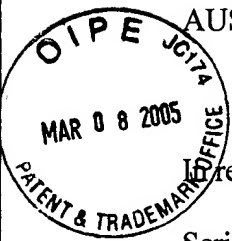


B118w

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PATENT APPLICATION
SERIAL NO. 09/978,358



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of: Bishop Chapman Brock

Serial Number: 09/978,358

Filed: October 16, 2001

For: A SYSTEM AND METHOD FOR
CONTROLLING A MULTIPLEXER FOR
SELECTING BETWEEN AND INPUT
CLOCK AND AN INPUT DUTY-CYCLE-
CORRECTED CLOCK AND
OUTPUTTING THE SELECTED CLOCK
AND AN ENABLE SIGNAL

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Group Art Unit: 2115

Examiner: Mark A. Connolly

M/S: ISSUE FEE
Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

CERTIFICATE OF MAILING

I hereby certify that the original paper identified herein
is being deposited with United States Postal Service as first class
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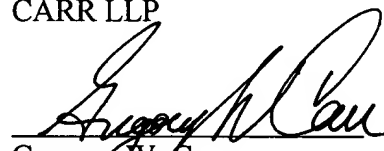
on: _____

LETTER TO OFFICIAL DRAFTSPERSON

Transmitted herewith are two (2) sheets of formal drawings which reflect the changes
required by the Examiner in the Office Action dated December 14, 2004.

Respectfully submitted,

CARR LLP


Gregory W. Carr
Reg. No. 31,093

Dated: 17/14/04
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